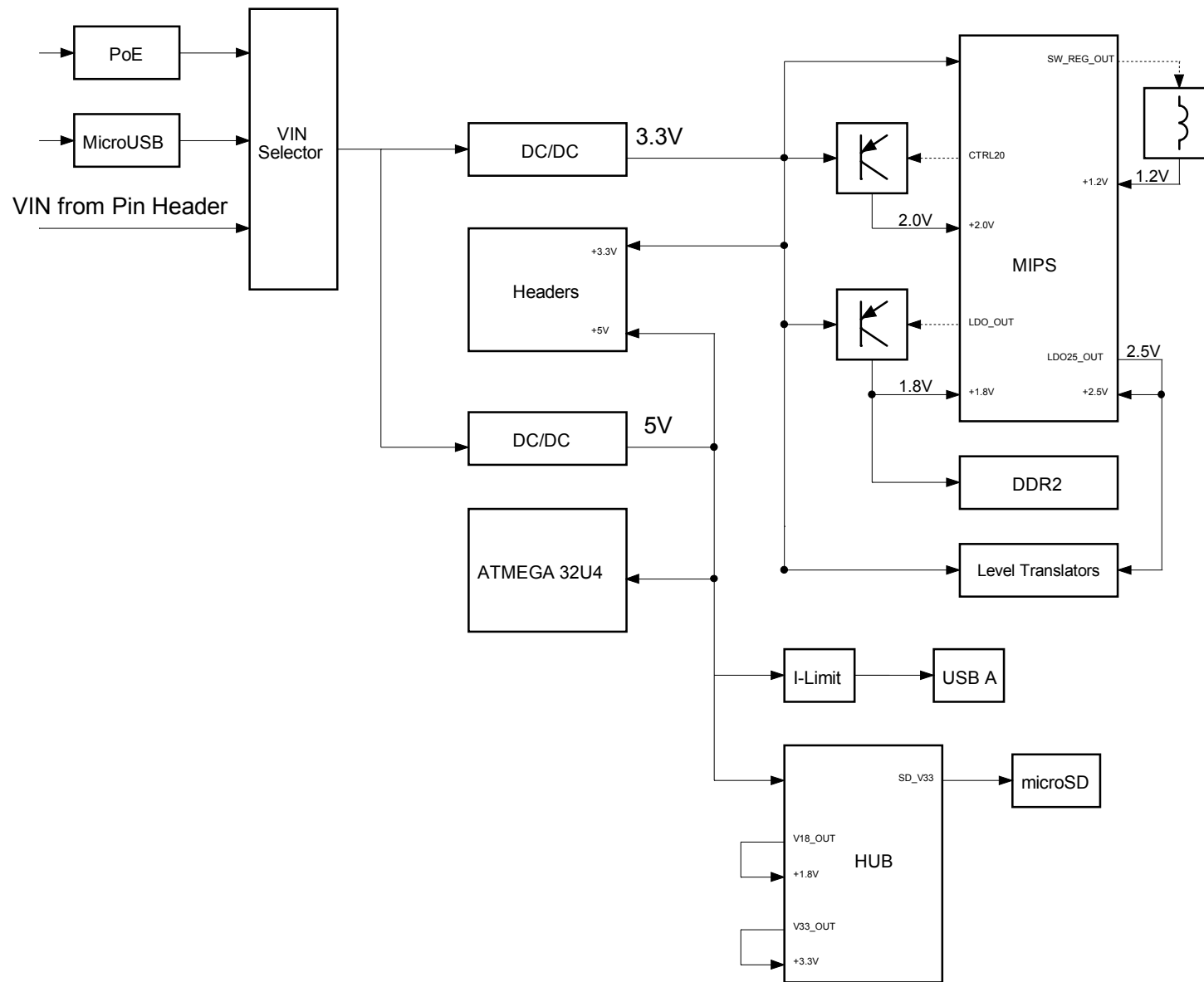
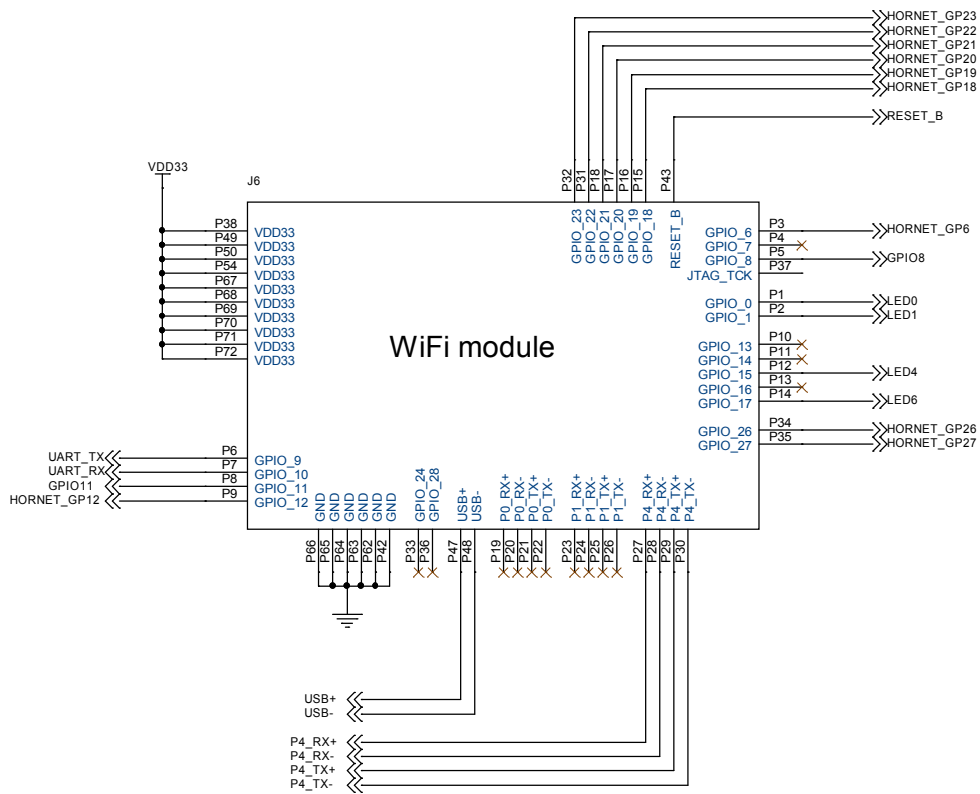


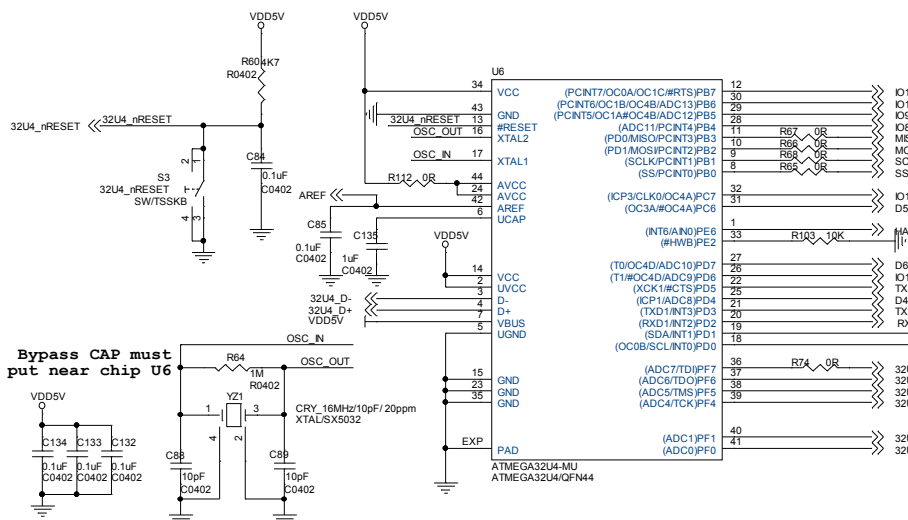
Power Block Diagram



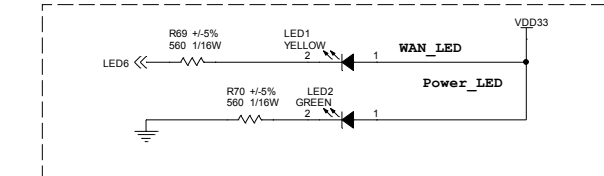
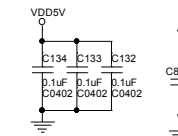


These files are licensed under a Creative Commons Attribution Share-Alike license, which allows for both personal and commercial derivative works, as long as they credit dog hunter LLC and release their designs under the same license. www.doghunter.org
 The Linino software is also open-source. The source code is released under the GPL and the C/C++ microcontroller libraries are under the LGPL. www.linino.org

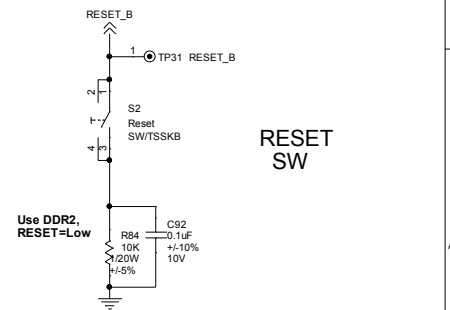
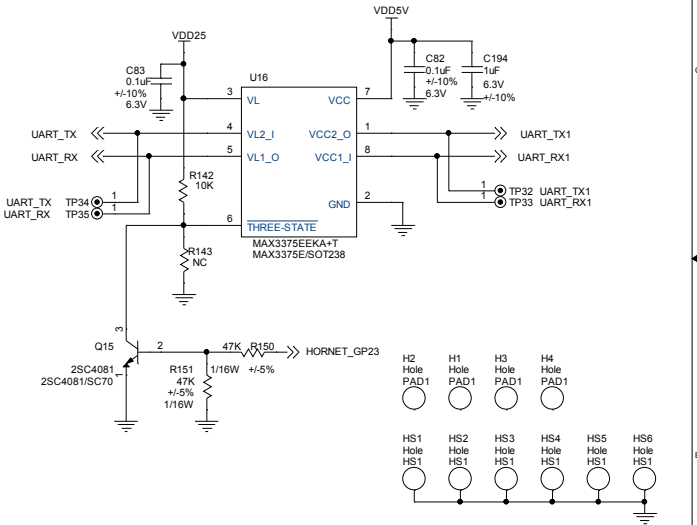
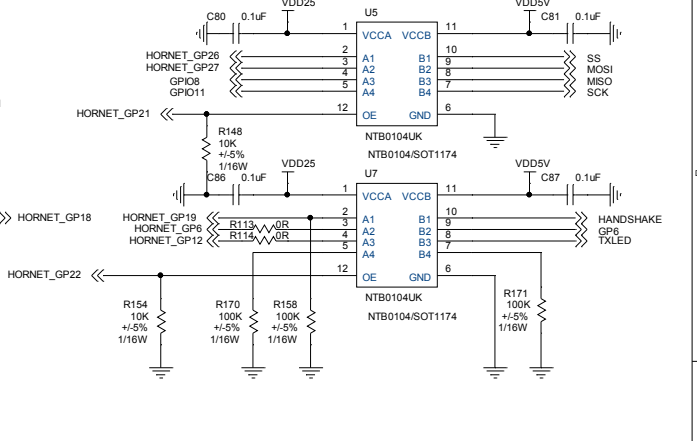
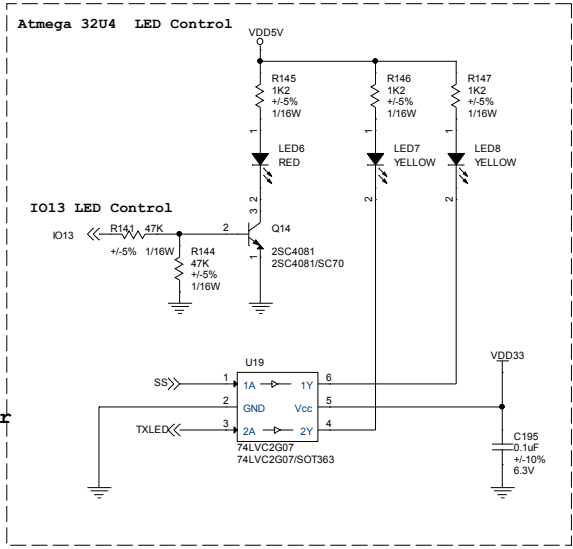
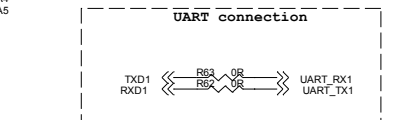
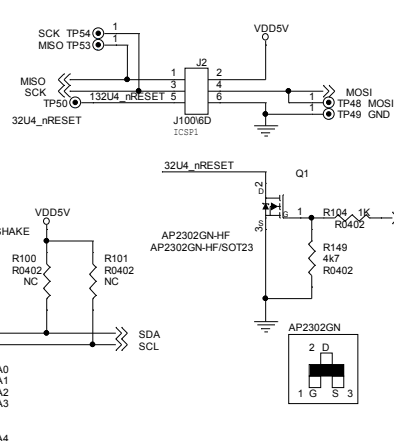
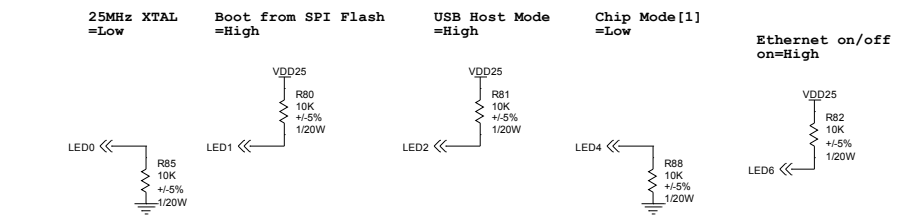
Title		
WiFi module		
Size	Document Number	Rev
Date:	Thursday, January 15, 2015	Sheet 4 of 10

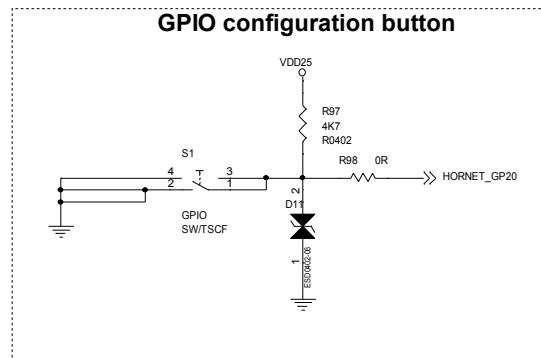
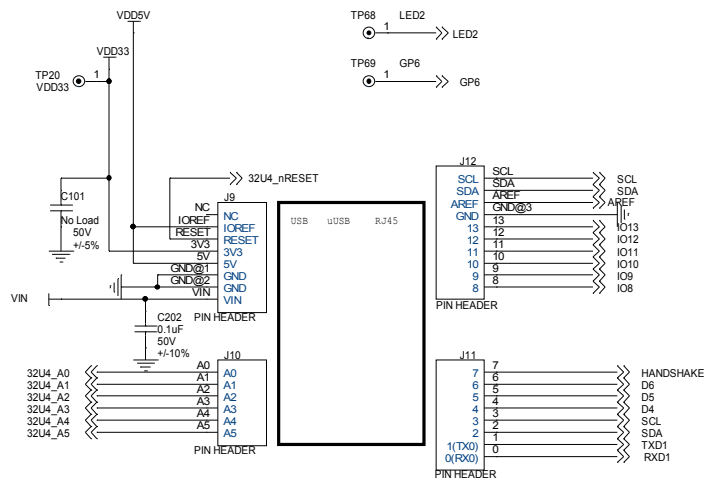
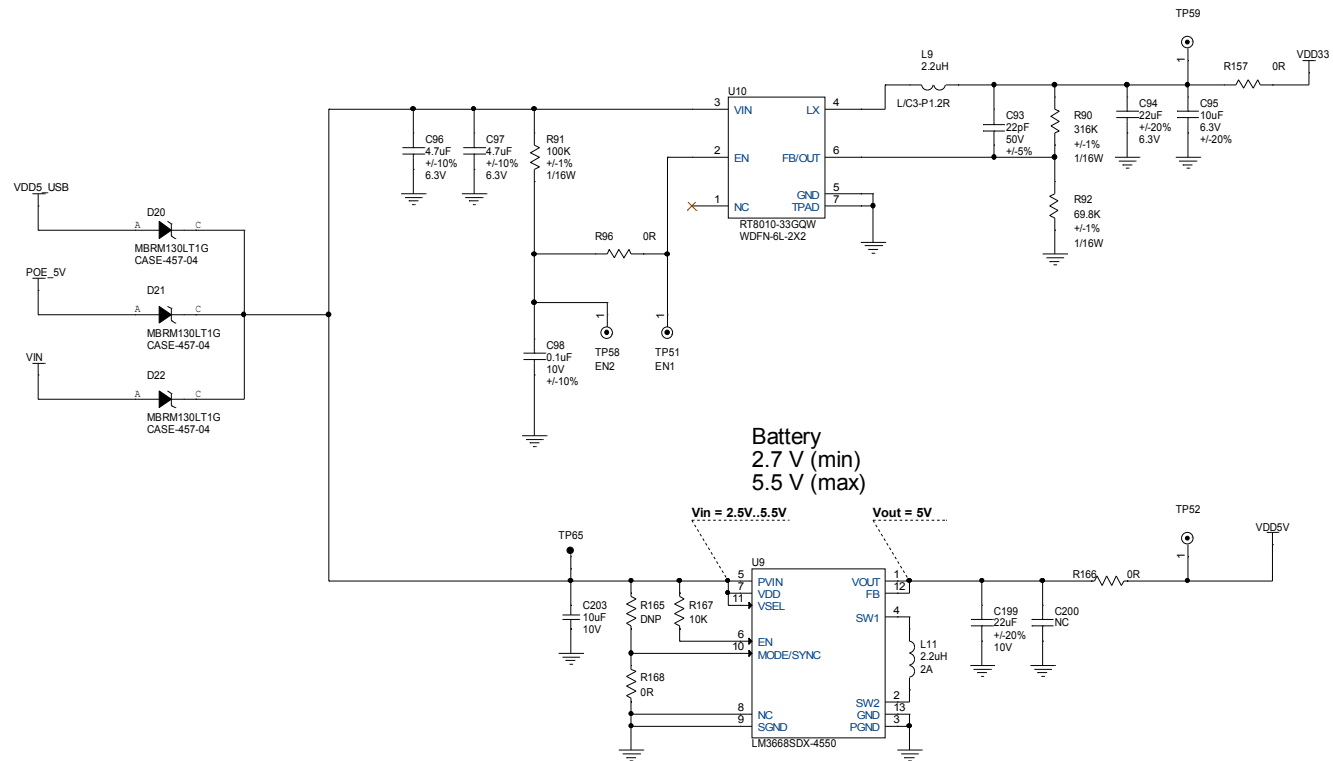


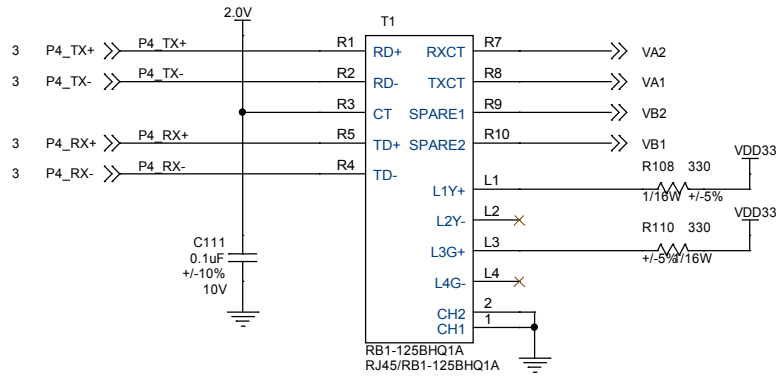
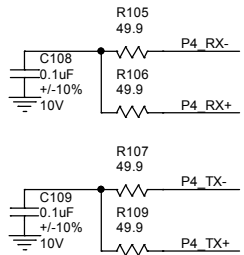
Bypass CAP must put near chip U6



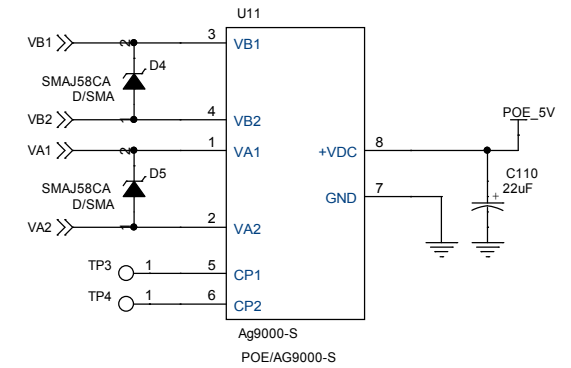
Trade-offs:
 Nets name LED0, LED1, LED2, LED3, LED4, LED6 are also for Boot-strap during power on. To avoid wrong voltage applied on the IO pin from LED, these GPIOs should be source current only. The rest LEDs are using sink current mode to reduce the Hornet internal 2.5V LDO loading (thermal).





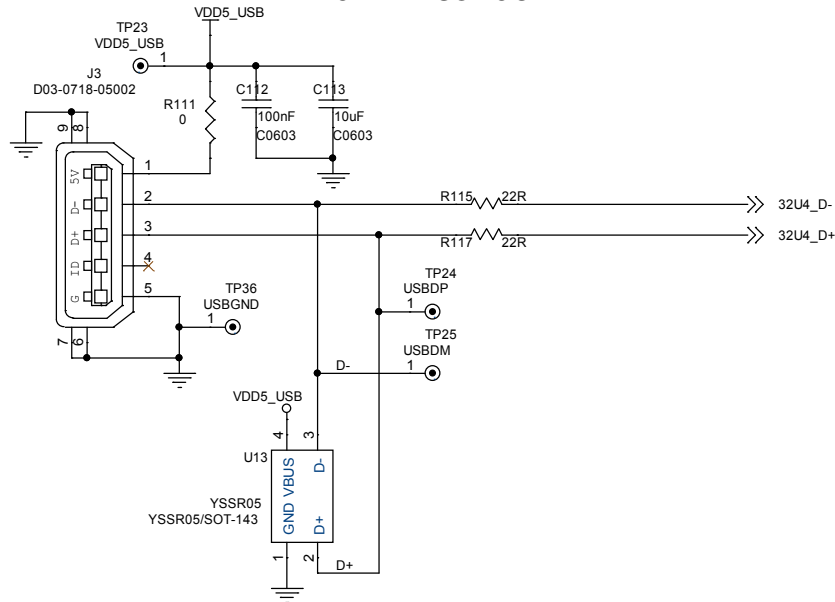


POE

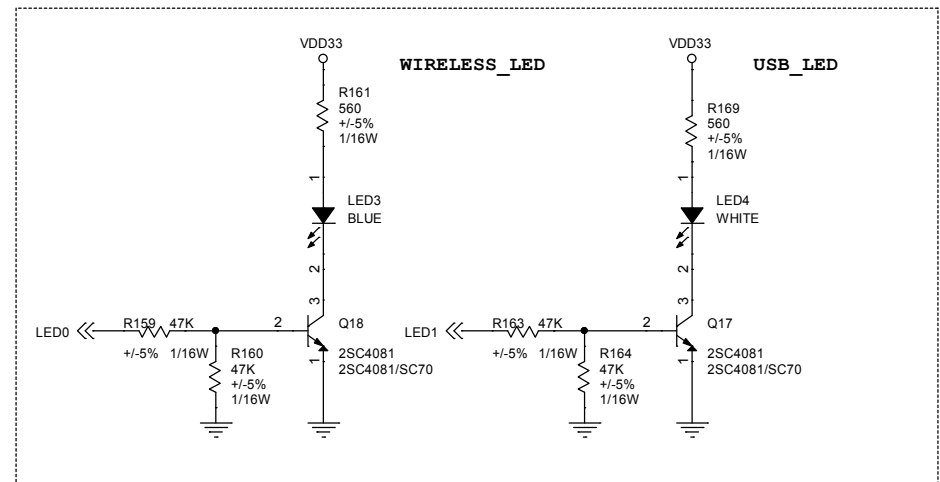


Add diodes D4&D5 for input protection

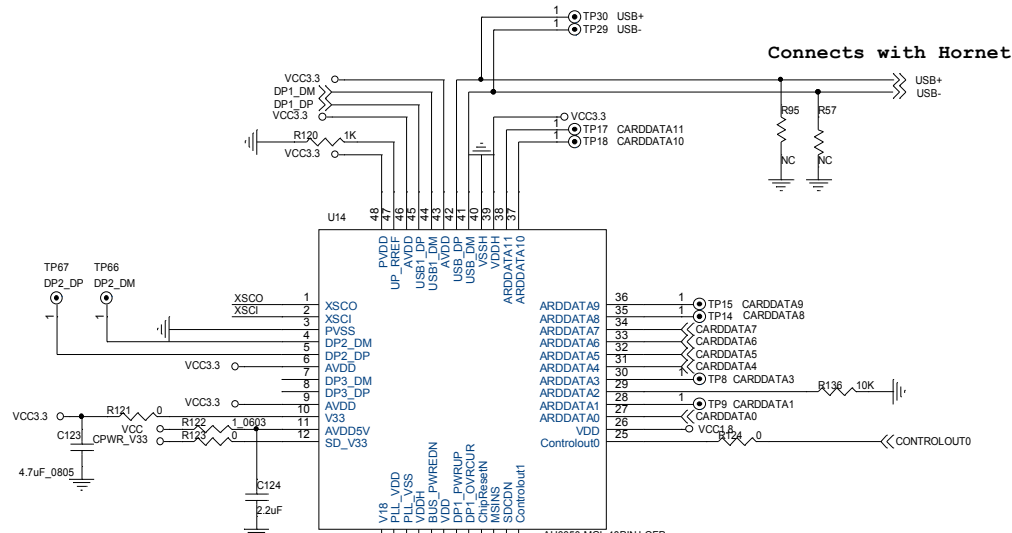
UART to USB



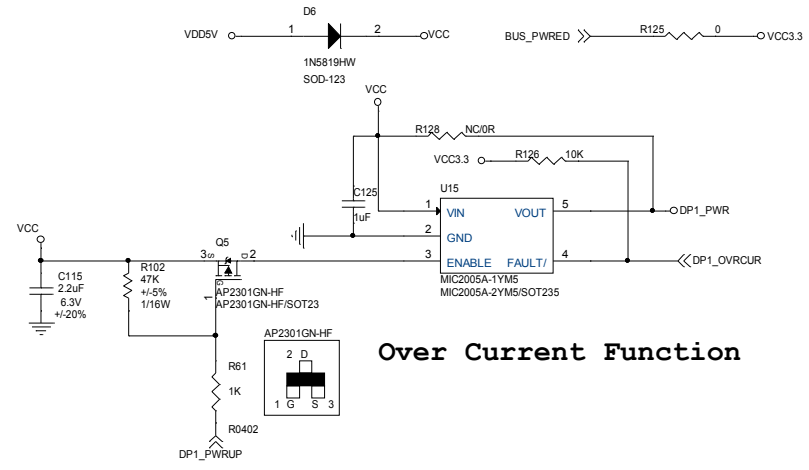
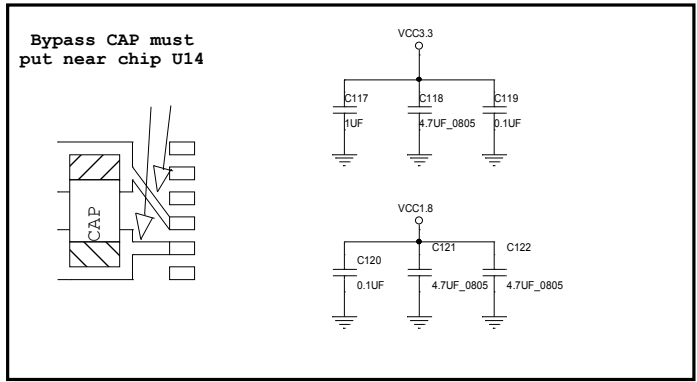
Micro USB



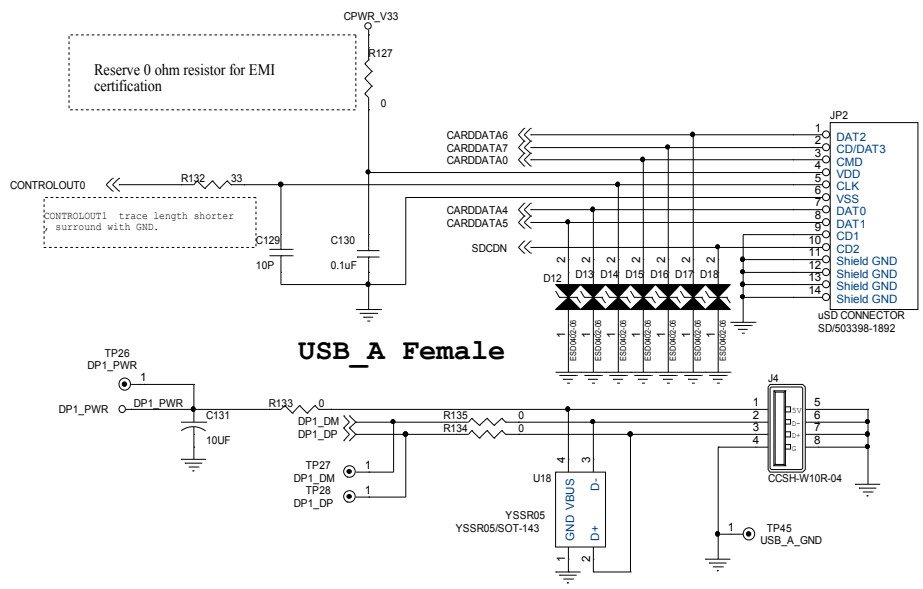
Title		
POE/Micro USB		
Size	Document Number	Rev
Date:	Thursday, January 15, 2015	Sheet 9 of 10



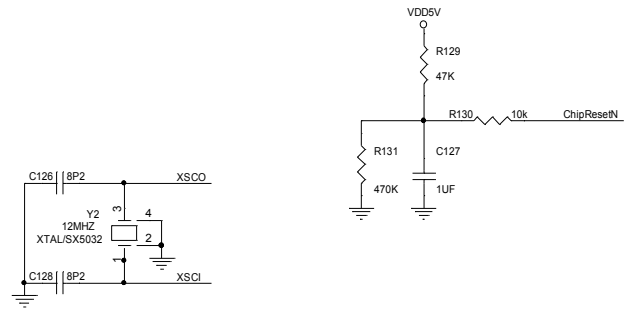
Connects with Hornet



Over Current Function



USB_A Female



Title	AU6350/SDUSB_A	
Size	Document Number	
Date:	Thursday, January 15, 2015	Sheet 10 of 10