

- Ground
- Power
- LED
- Internal Pin
- SWD Pin
- Digital Pin
- Analog Pin
- Other Pin
- Microcontroller's Port
- Default
- FPGA Port

**If programmed with SAMD**

- ⚠ **MAXIMUM** current per pin is 7mA
- ⚠ **MAXIMUM** source current is 46mA
- ⚠ **MAXIMUM** sink current is 65mA per pin group

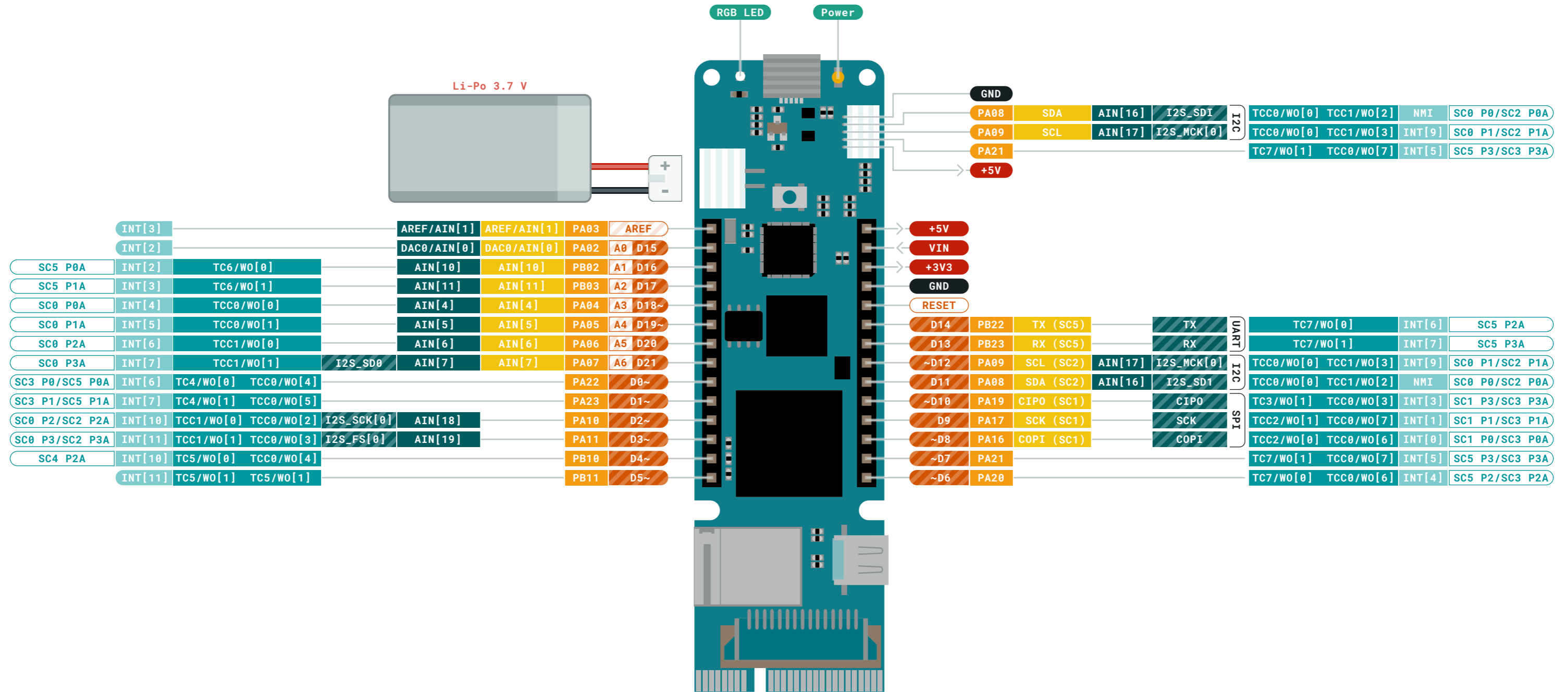
**If programmed with FPGA**

- ⚠ **MAXIMUM** current if the I/O standard configuration 3.3-V LVTTTL is 4mA
- ⚠ **MAXIMUM** current if the I/O standard configuration 3.3-V LVCMOS is 2mA

**VIN** Input voltage to the board.

NOTE: CIPO/COPI have previously been referred to as MISO/MOSI





Ground	Digital Pin	Analog
Power	Analog Pin	Communication
LED	Other Pin	Timer
Internal Pin	Microcontroller's Port	Interrupt
SWD Pin	Default	Sercom
	FPGA Port	

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- MAXIMUM** current per pin is 7mA
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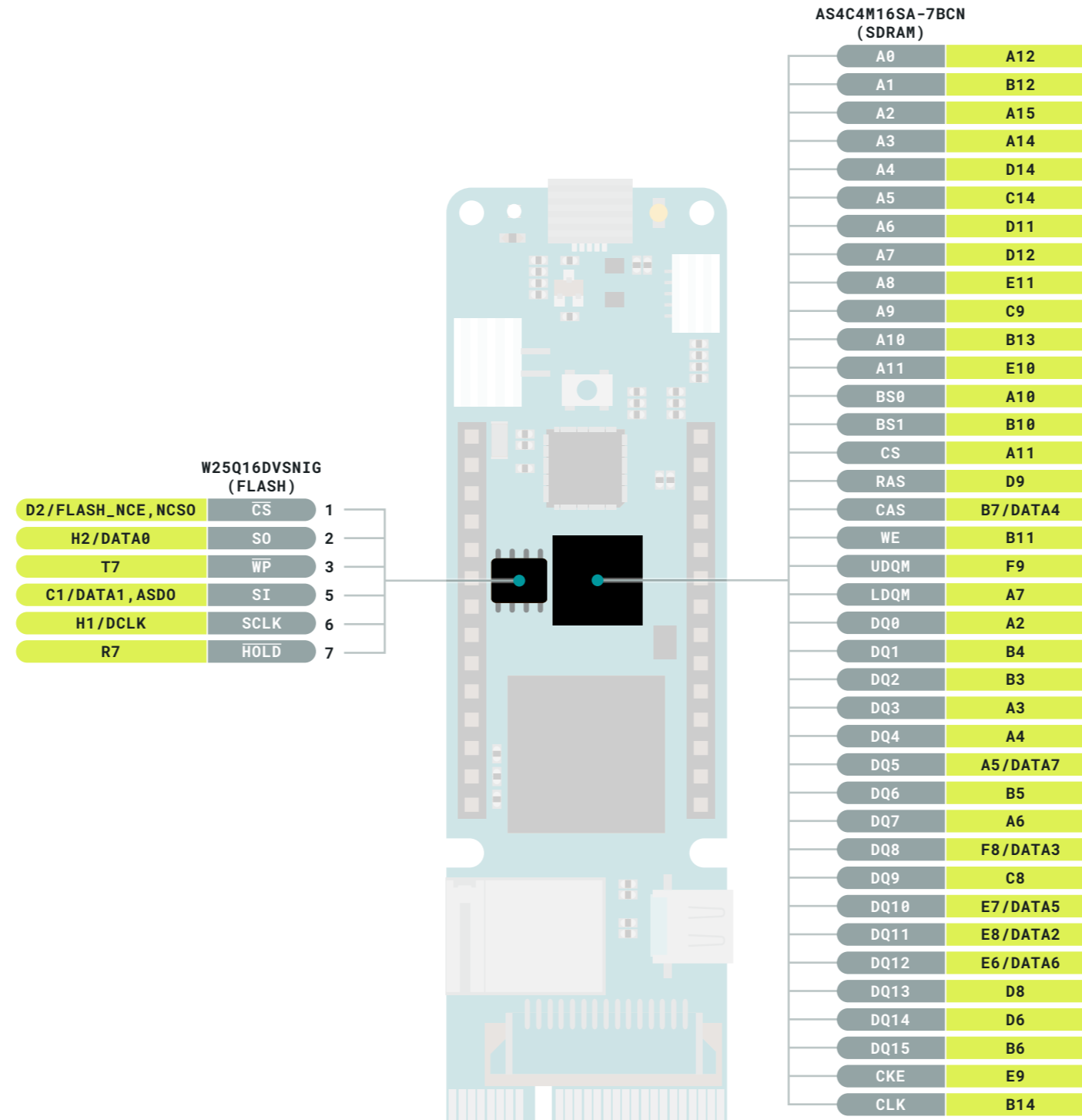
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**VIN** Input voltage to the board.

NOTE: CIPO/COPI have previously been referred to as MISO/MOSI





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- Analog Pin
- Other Pin
- Microcontroller's Port
- Default
- FPGA Port

**If programmed with SAMD**

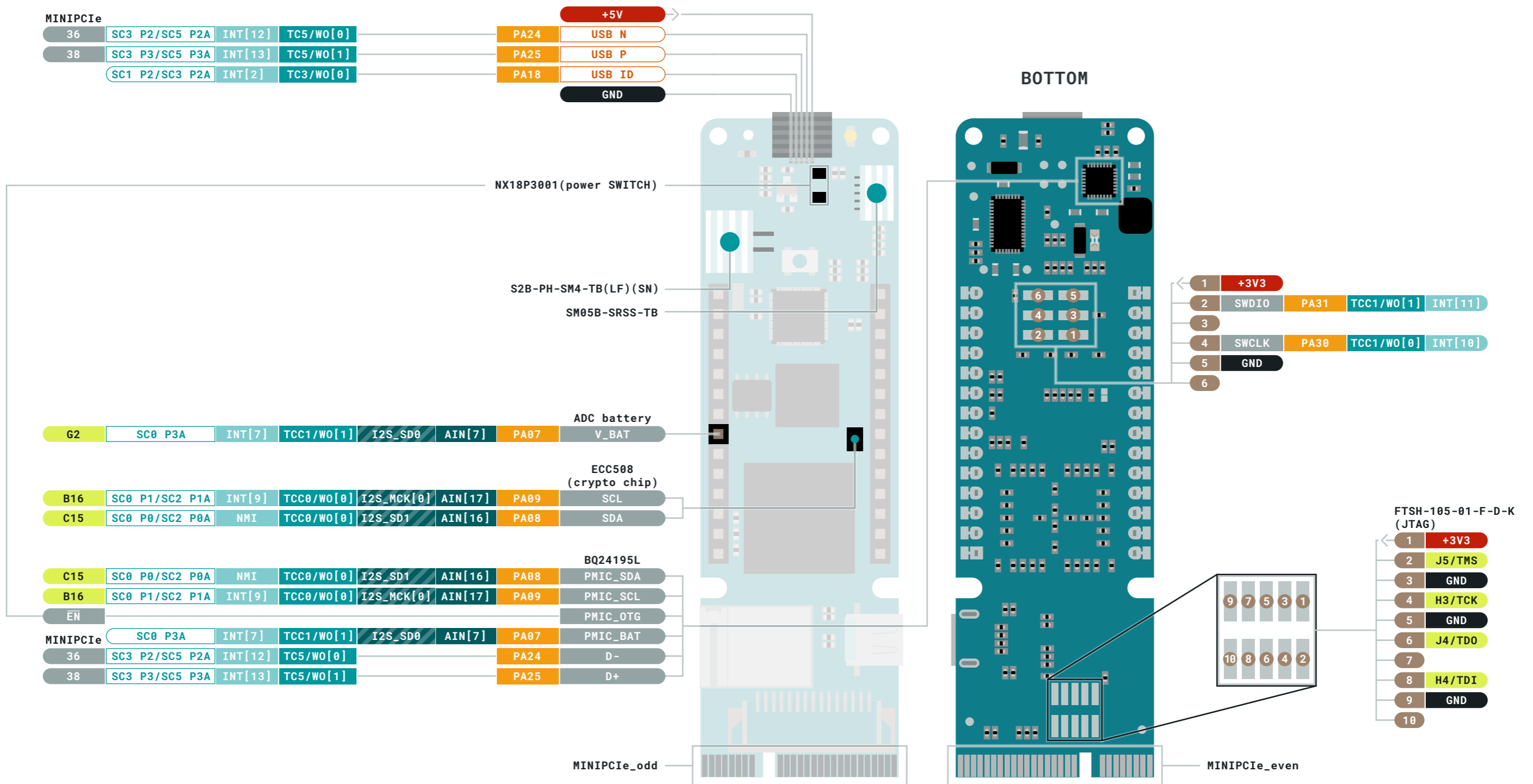
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**If programmed with FPGA**

- ⚠ **MAXIMUM** current if the I/O standard configuration 3.3-V LVTTL is 4mA
- ⚠ **MAXIMUM** current if the I/O standard configuration 3.3-V LVCMOS is 2mA

**VIN** Input voltage to the board.





Ground	Digital Pin	Analog
Power	Analog Pin	Communication
LED	Other Pin	Timer
Internal Pin	Microcontroller's Port	Interrupt
SWD Pin	Default	Sercom
	FPGA Port	

**If programmed with SAMD**

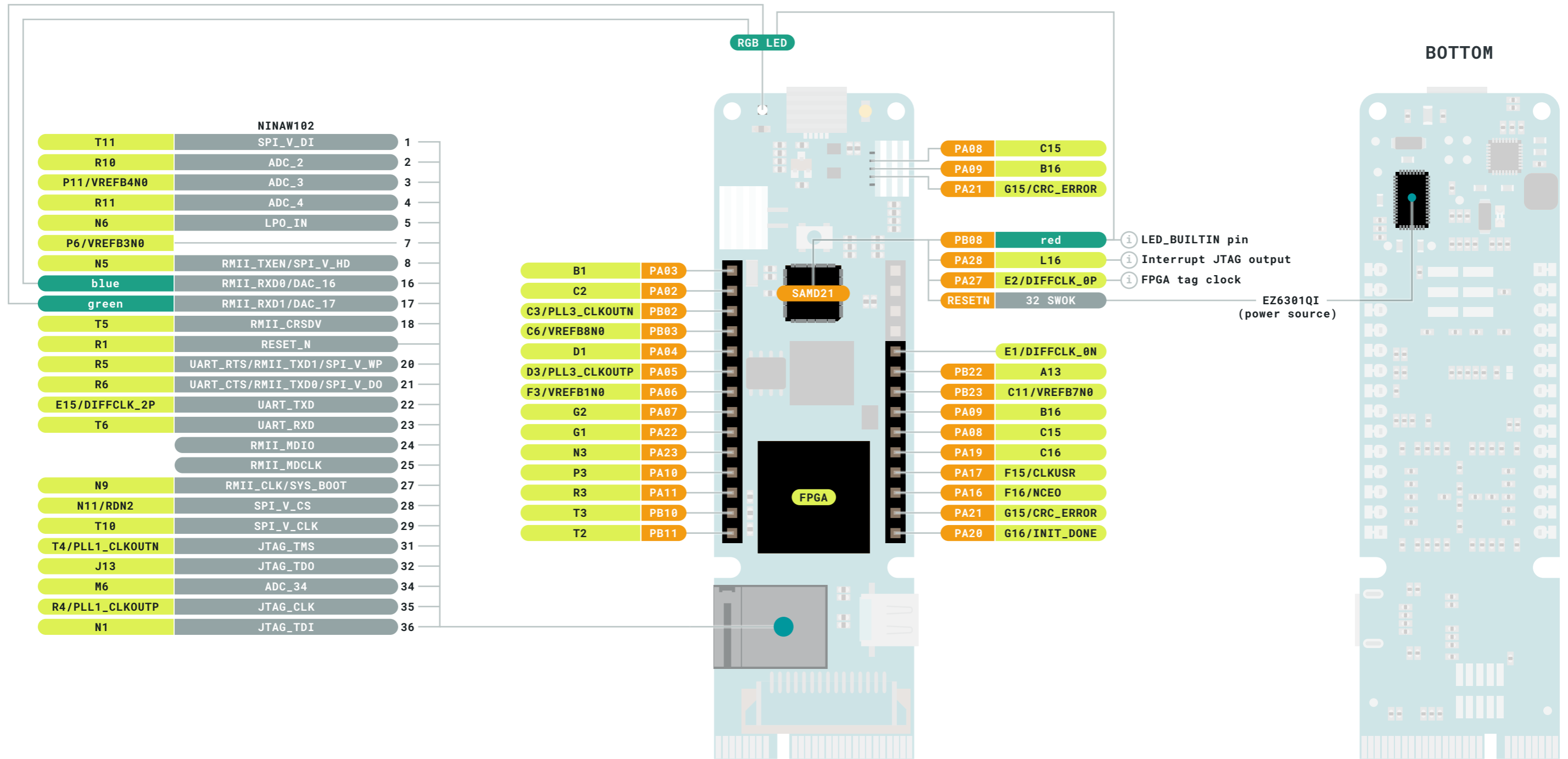
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NINAW102		
T11	SPI_V_DI	1
R10	ADC_2	2
P11/VREFB4N0	ADC_3	3
R11	ADC_4	4
N6	LPO_IN	5
P6/VREFB3N0		7
N5	RMII_TXEN/SPI_V_HD	8
blue	RMII_RXD0/DAC_16	16
green	RMII_RXD1/DAC_17	17
T5	RMII_CRSDV	18
R1	RESET_N	
R5	UART_RTS/RMII_TXD1/SPI_V_WP	20
R6	UART_CTS/RMII_TXD0/SPI_V_DO	21
E15/DIFFCLK_2P	UART_TXD	22
T6	UART_RXD	23
	RMII_MDIO	24
	RMII_MDCLK	25
N9	RMII_CLK/SYS_BOOT	27
N11/RDN2	SPI_V_CS	28
T10	SPI_V_CLK	29
T4/PLL1_CLKOUTN	JTAG_TMS	31
J13	JTAG_TDO	32
M6	ADC_34	34
R4/PLL1_CLKOUTP	JTAG_CLK	35
N1	JTAG_TDI	36

B1	PA03
C2	PA02
C3/PLL3_CLKOUTN	PB02
C6/VREFB8N0	PB03
D1	PA04
D3/PLL3_CLKOUTP	PA05
F3/VREFB1N0	PA06
G2	PA07
G1	PA22
N3	PA23
P3	PA10
R3	PA11
T3	PB10
T2	PB11

PA08	C15
PA09	B16
PA21	G15/CRC_ERROR
PB08	red
PA28	L16
PA27	E2/DIFFCLK_0P
RESETN	32 SWOK
E1/DIFFCLK_0N	
PB22	A13
PB23	C11/VREFB7N0
PA09	B16
PA08	C15
PA19	C16
PA17	F15/CLKUSR
PA16	F16/NCEO
PA21	G15/CRC_ERROR
PA20	G16/INIT_DONE

- LED\_BUILTIN pin
- Interrupt JTAG output
- FPGA tag clock
- EZ6301QI (power source)

Ground	Digital Pin
Power	Analog Pin
LED	Other Pin
Internal Pin	Microcontroller's Port
SWD Pin	Default
	FPGA Port

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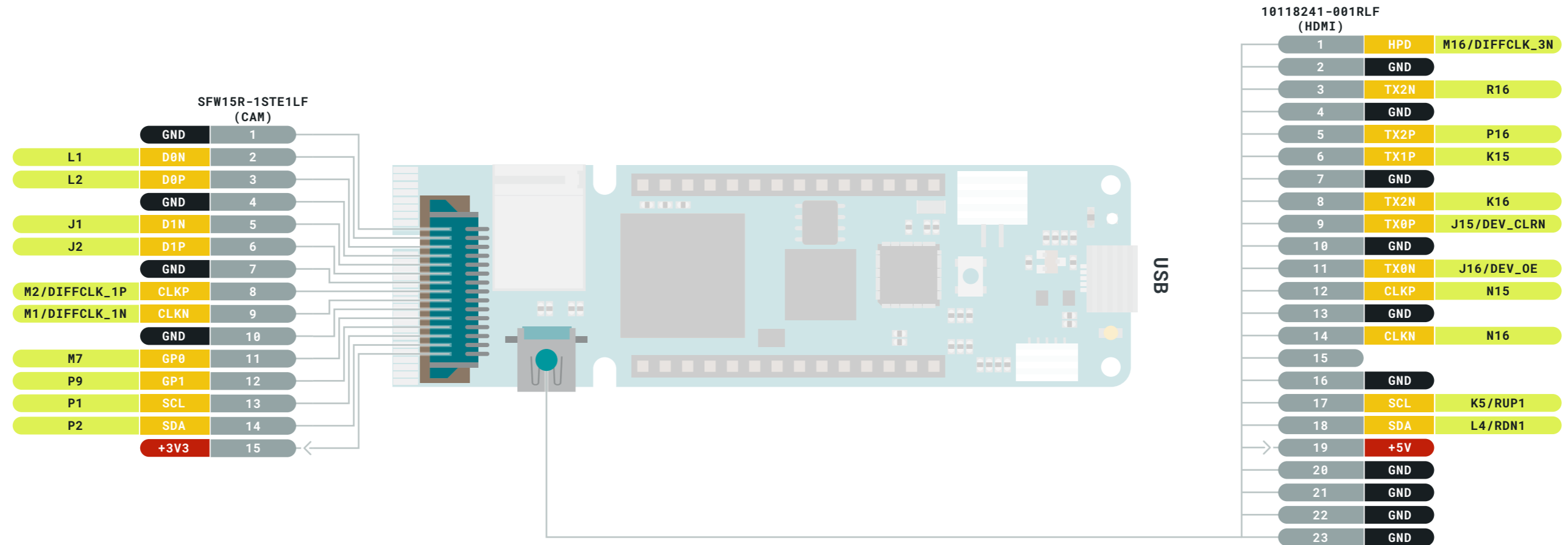
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**VIN** Input voltage to the board.



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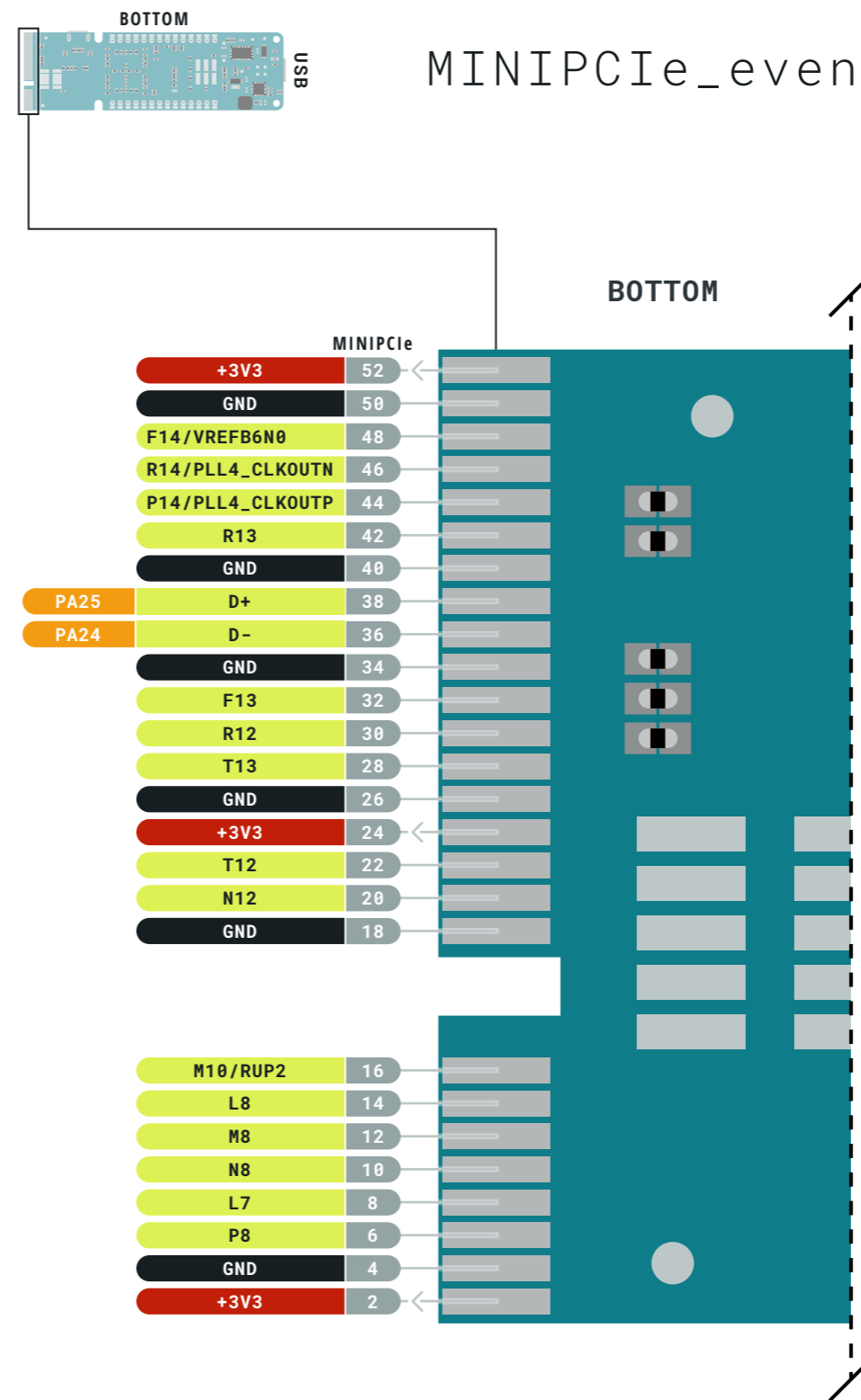
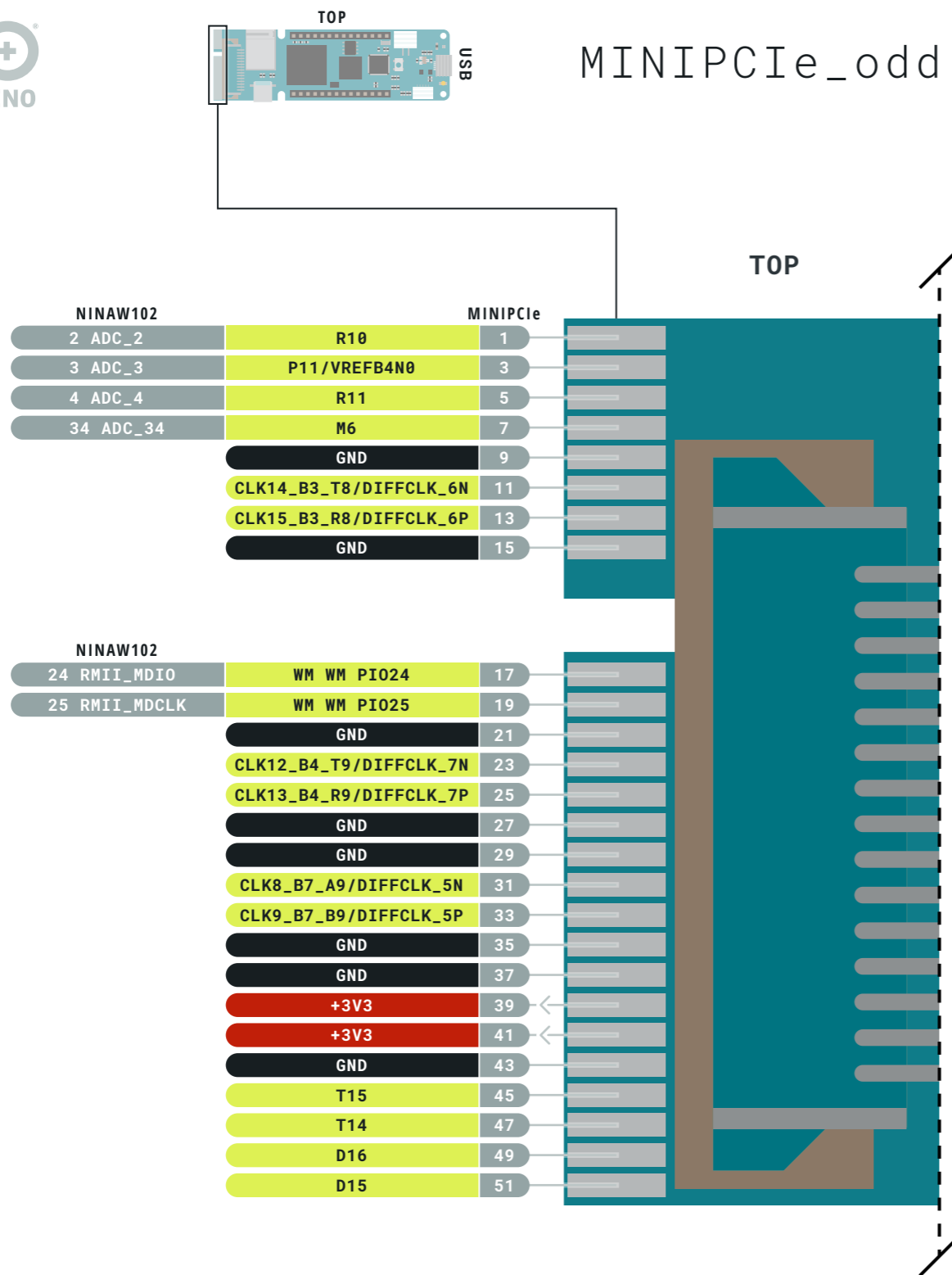
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